

Amendment to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. - 8. (canceled)

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9. (new) A digital signal receiver apparatus connected to external digital signal transmitter apparatuses by circuit of a digital signal bus to receive a digital signal, wherein said digital signal receiver apparatus comprises an interface circuit for requesting authentication at a predetermined timing and then performing the authentication to said external digital signal transmitter apparatuses by converting an instruction from a control circuit to a transmission packet in a packet processing circuit via an asynchronous transmission processing circuit and transmitting said transmission packet to said digital signal bus via a digital signal bus input/output circuit by a bus control circuit.

10. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses, when said digital signal receiver apparatus is turned on.

11. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses, when said external digital signal transmitter apparatuses are turned on.

a' 12. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses, when said digital signal receiver apparatus is connected to said digital signal bus.

13. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses, when said external digital signal transmitter apparatuses are connected to said digital signal bus.

14. (new) A digital signal receiver apparatus according to claim 9, further comprising at least one input terminals including an input terminal from said digital signal bus and a switching circuit for selecting said input terminals,

wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses, when said input terminal connected to said digital signal bus is selected by said switching circuit.

15. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit retrieves said digital signal bus and requests authentication and then performs the authentication to said external digital signal transmitter apparatuses connected to said digital signal bus.

a! 16. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses transmitting a digital video signal and/or a digital audio signal.

17. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses transmitting a digital video signal and/or a digital audio signal, when said digital video signal and/or digital audio signal cannot be copied freely.

18. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to said external digital signal transmitter apparatuses which were previously registered.

19. (new) A digital signal receiver apparatus according to claim 9, wherein said interface circuit requests authentication and then performs the authentication to

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said external digital signal transmitter apparatuses which received a digital video
signal and/or a digital audio signal in the past.
